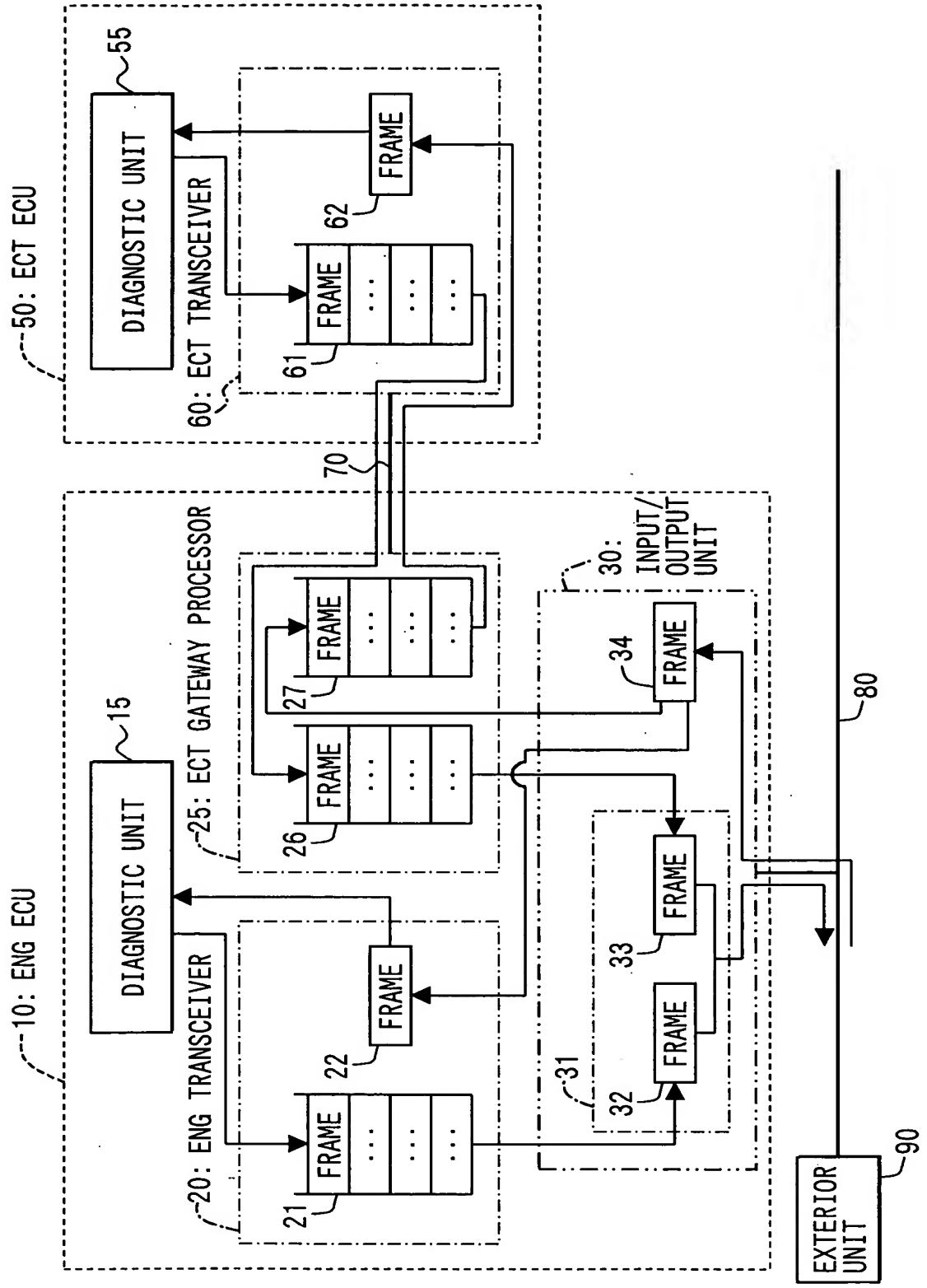


FIG. 1



**FIG. 2A**

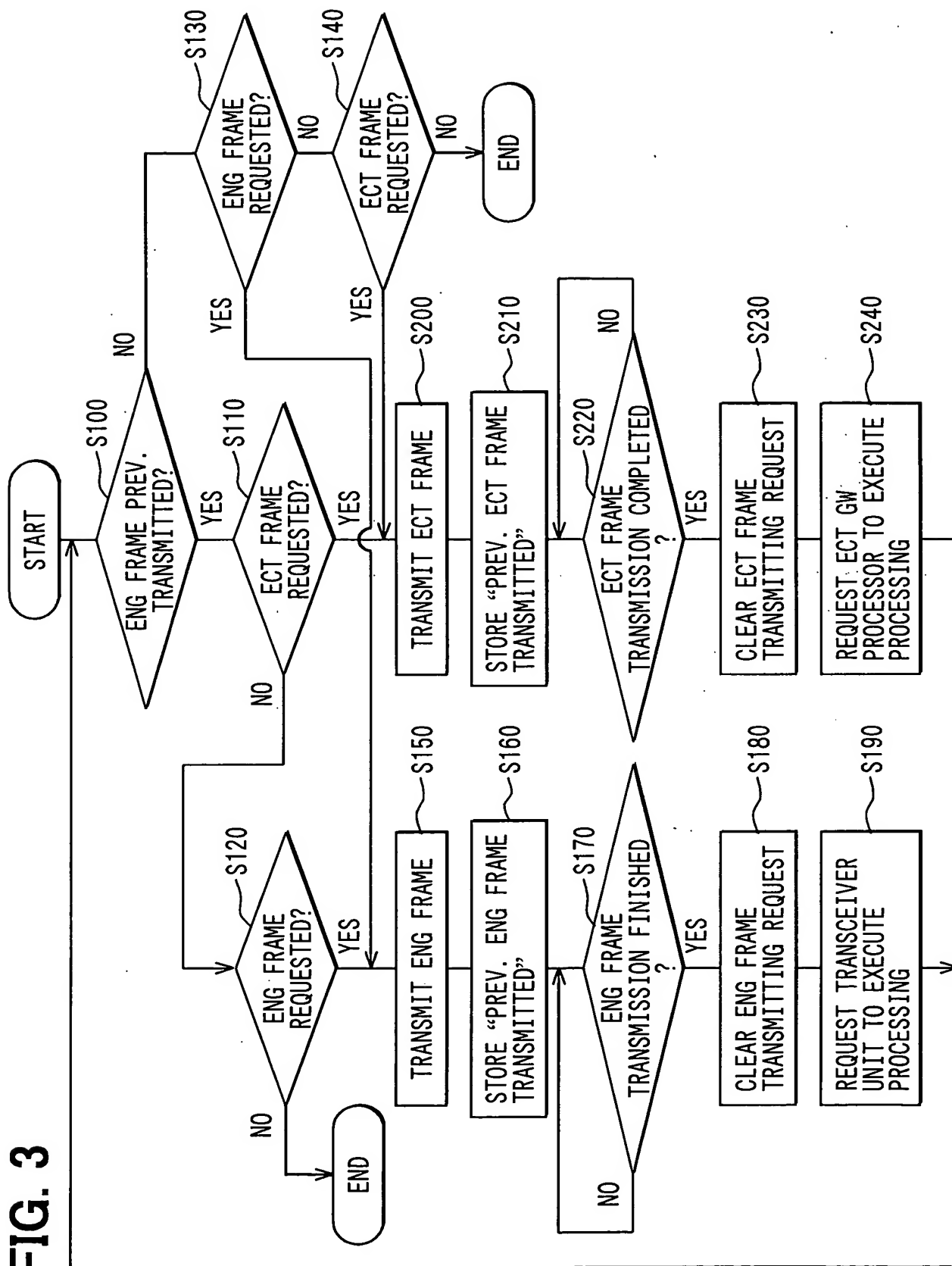
SINGLE FRAME	#0	#1	#2	#3	#4	#5	#6	#7
	PCI	DIAGNOSTIC DATA						

**FIG. 2B**

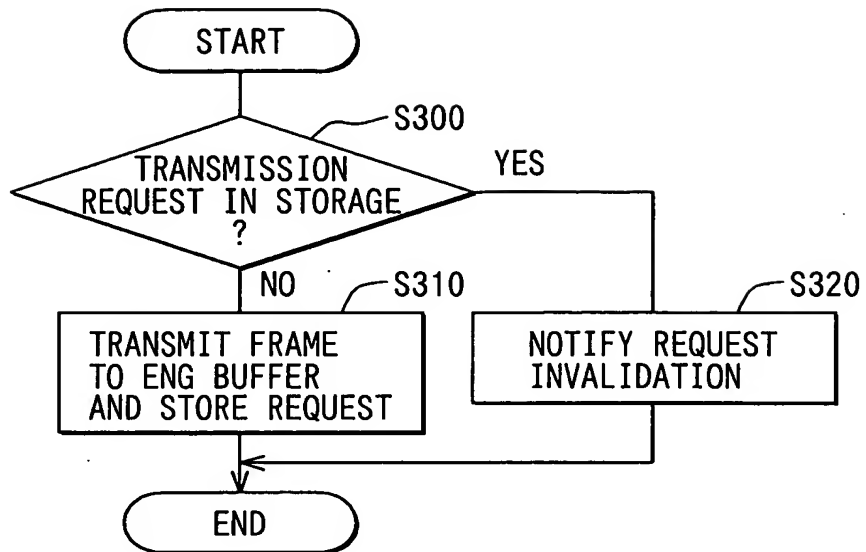
	#0	#1	#2	#3	#4	#5	#6	#7
FIRST FRAME	PCI		DIAGNOSTIC DATA					
CONSECUTIVE FRAME	PCI	DIAGNOSTIC DATA						

**FIG. 2C**

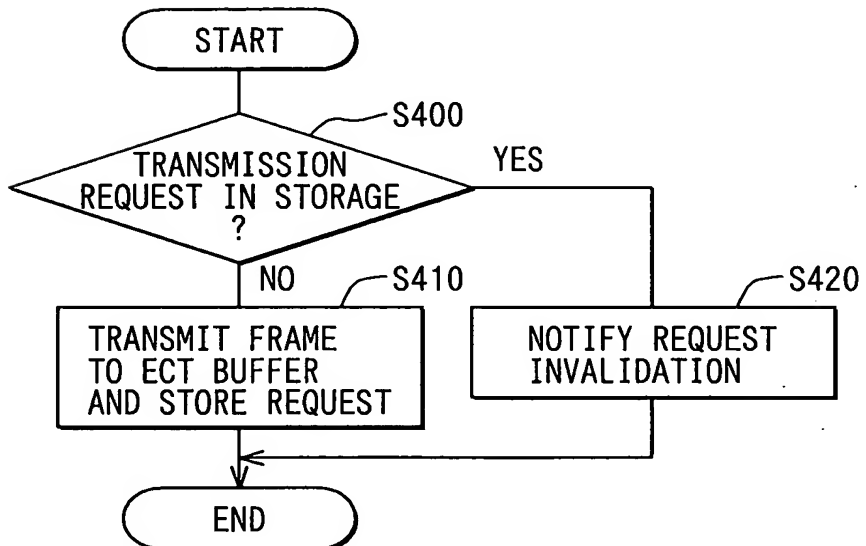
FLOW CONTROL FRAME	#0	#1	#2	#3	#4	#5	#6	#7
	PCI			UNUSED				

[illegible]

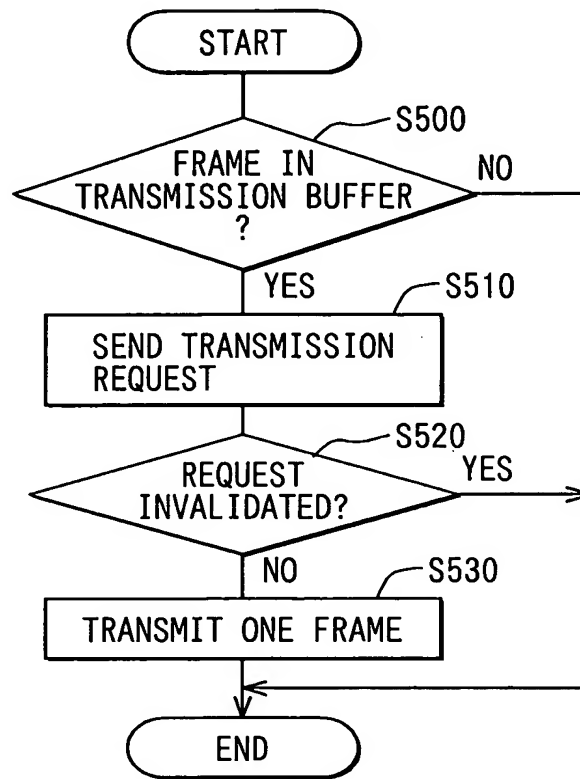
**FIG. 4A**



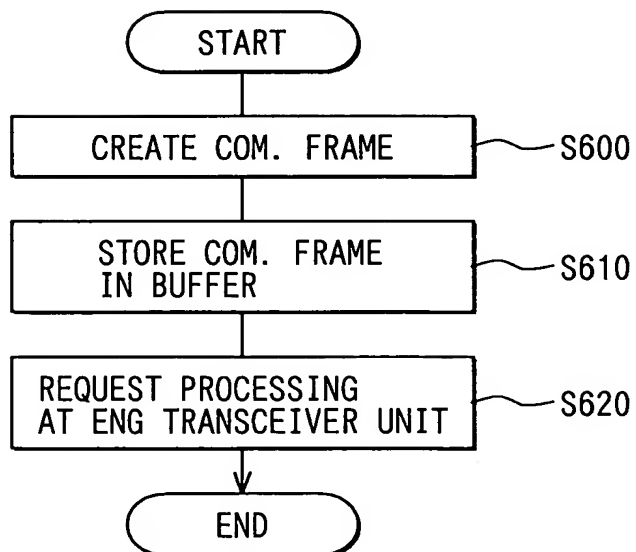
**FIG. 4B**



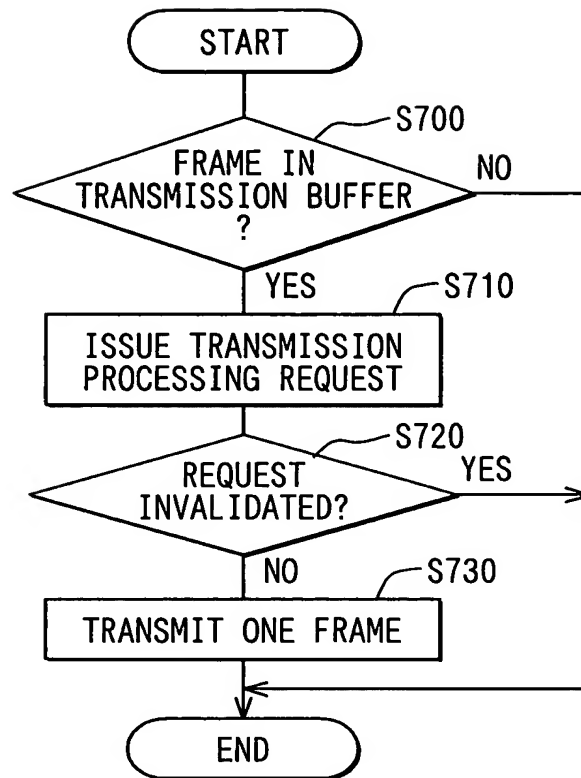
**FIG. 5A**



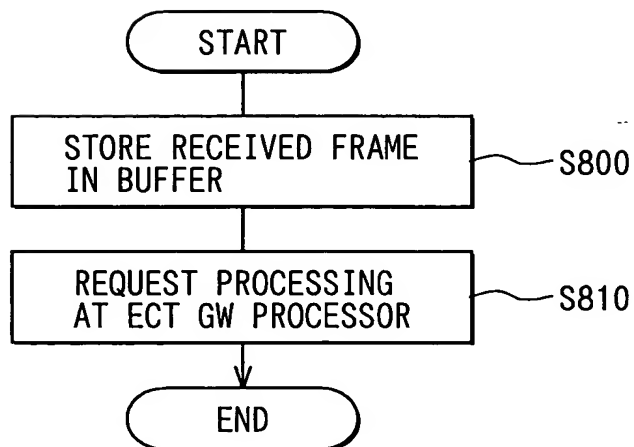
**FIG. 5B**



**FIG. 6A**



**FIG. 6B**



**FIG. 7**

